

Figure 1

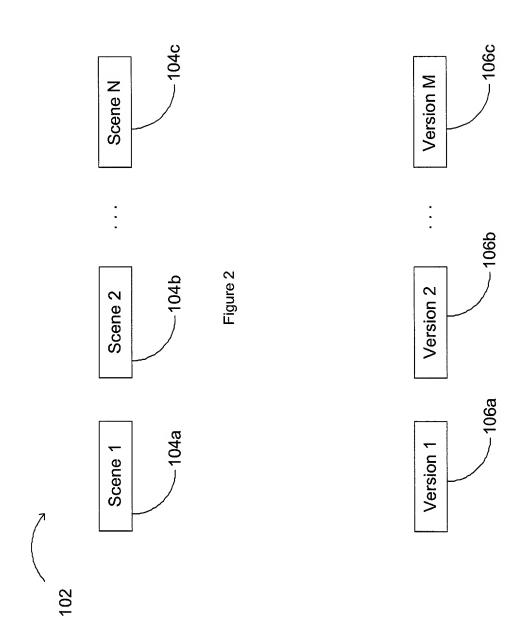


Figure 3

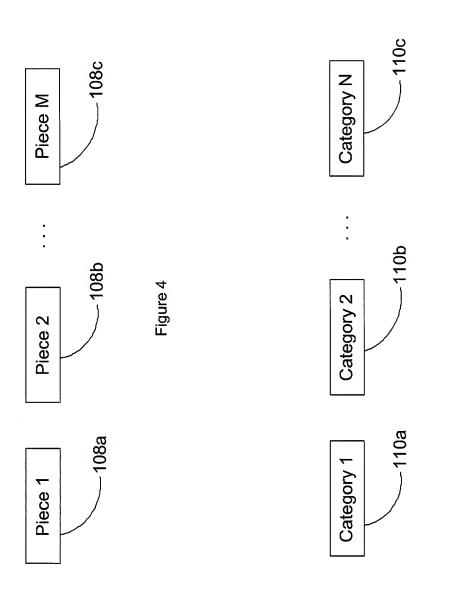


Figure 5

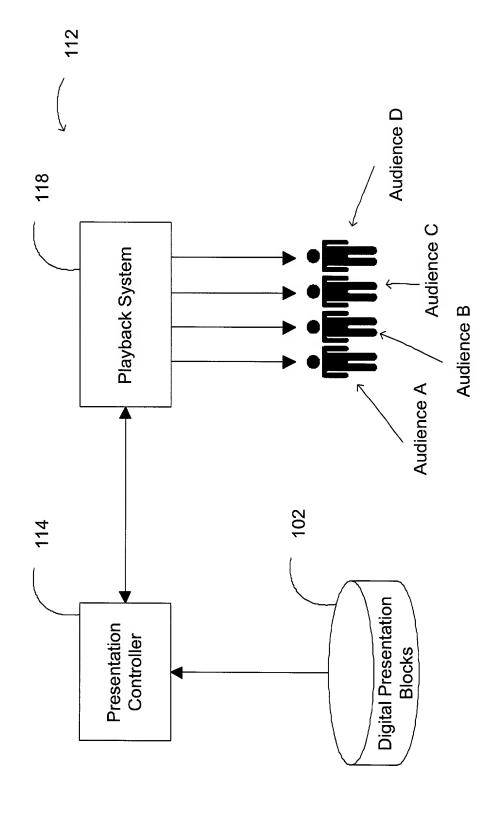


Figure 6

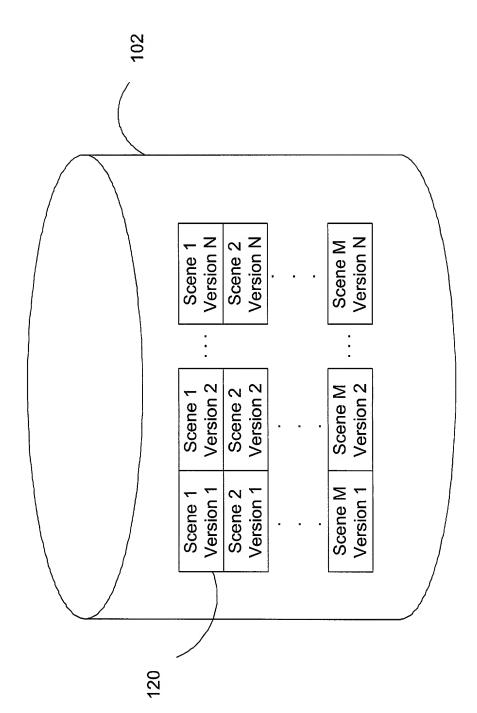


Figure 7

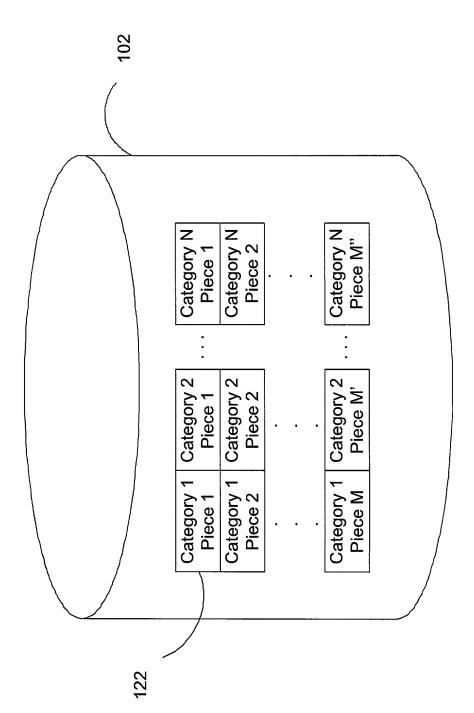
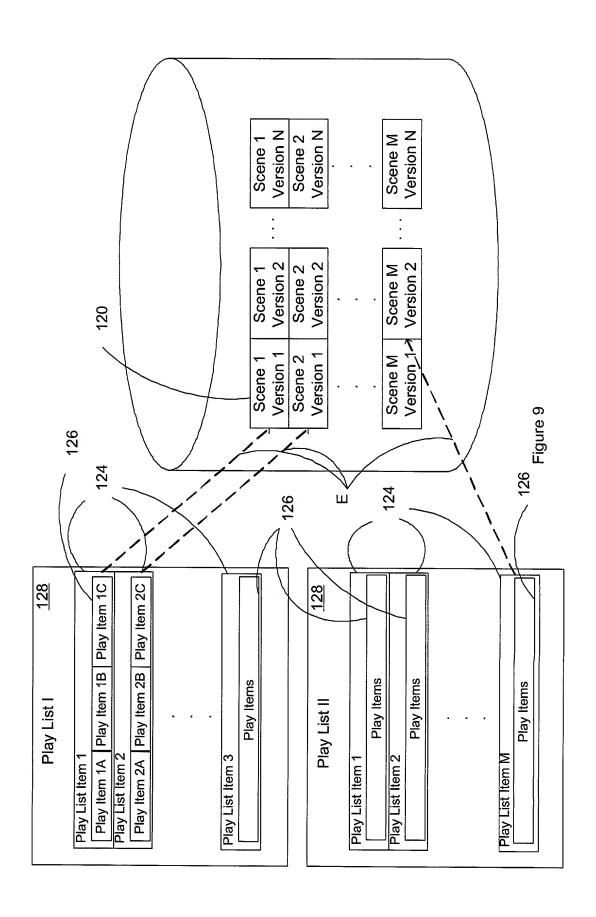
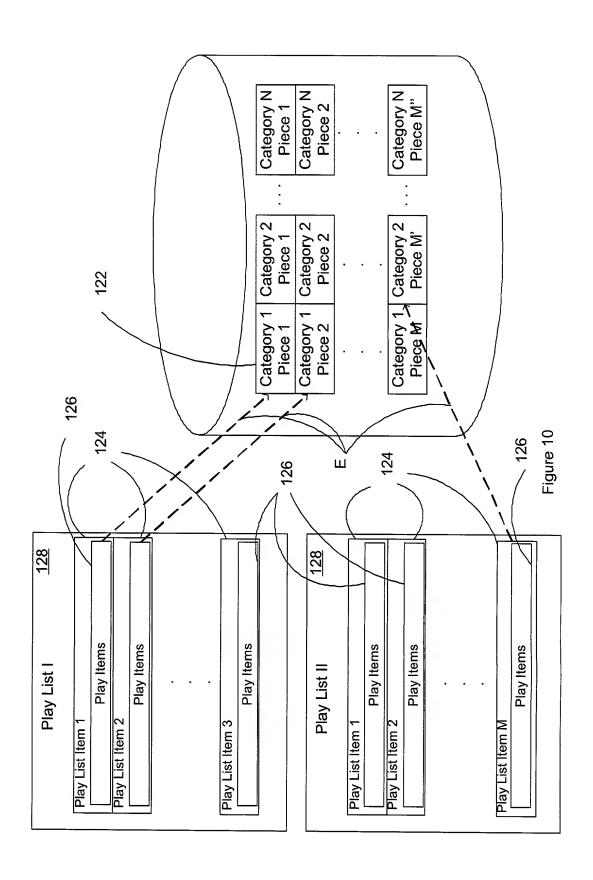


Figure 8





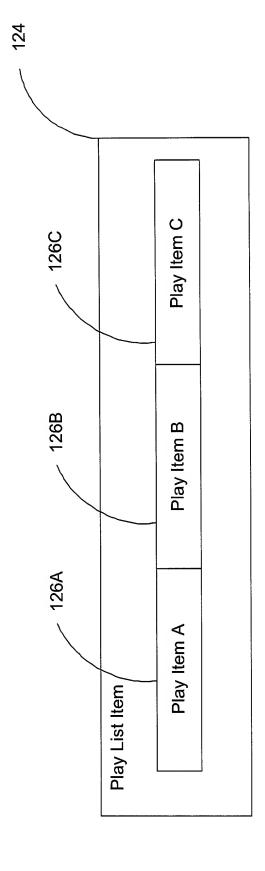


Figure 11

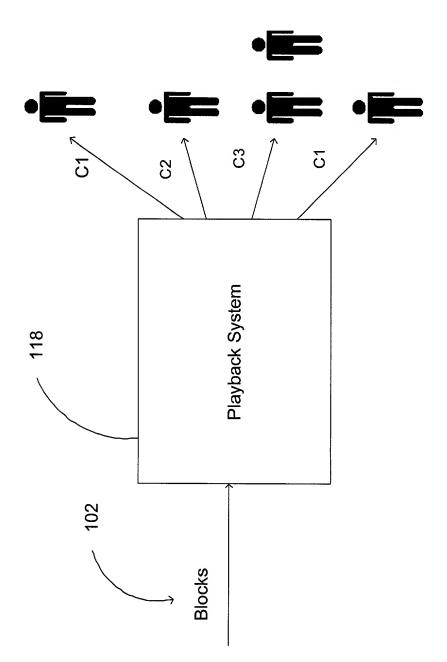
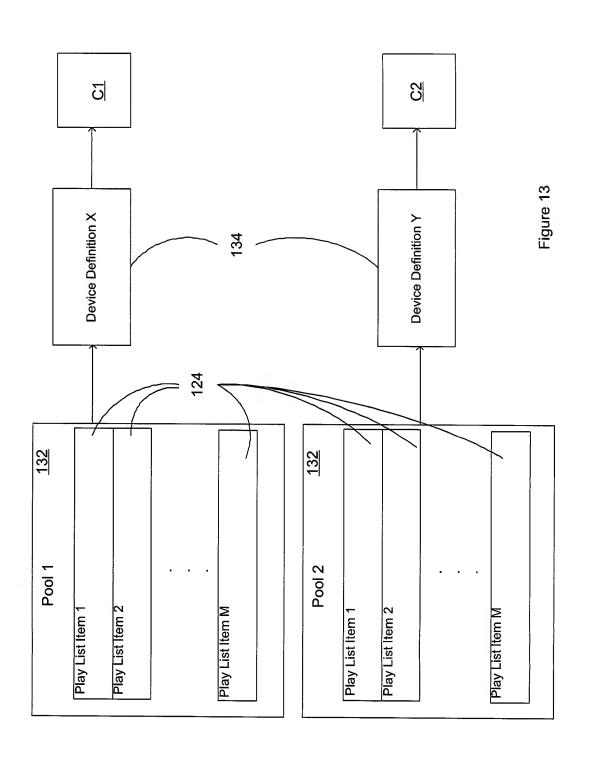


Figure 12



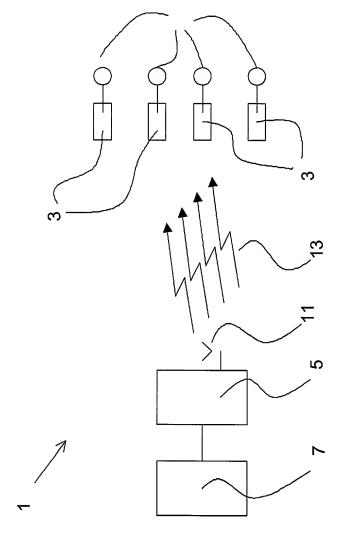


Figure 14

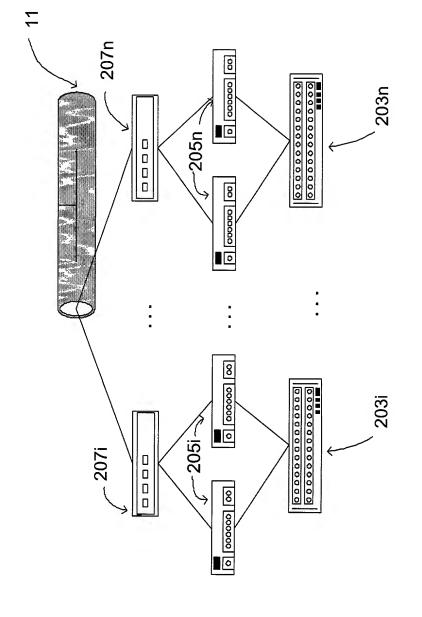


Figure 15

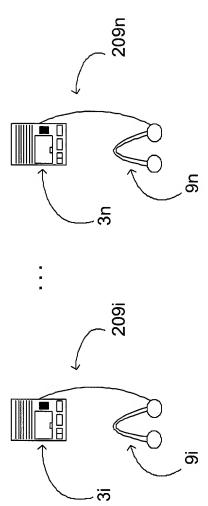


Figure 16

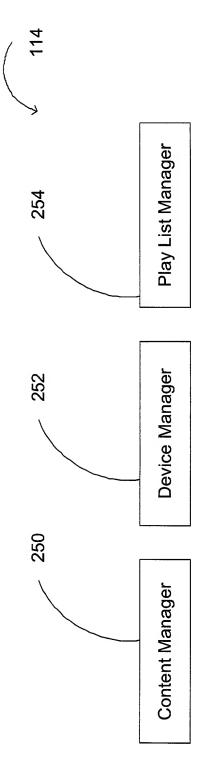


Figure 17

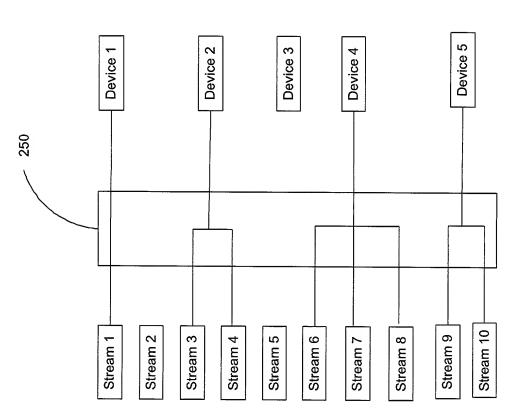


Figure 18

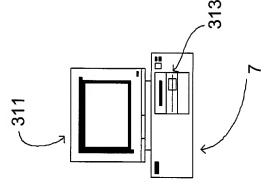


Figure 19

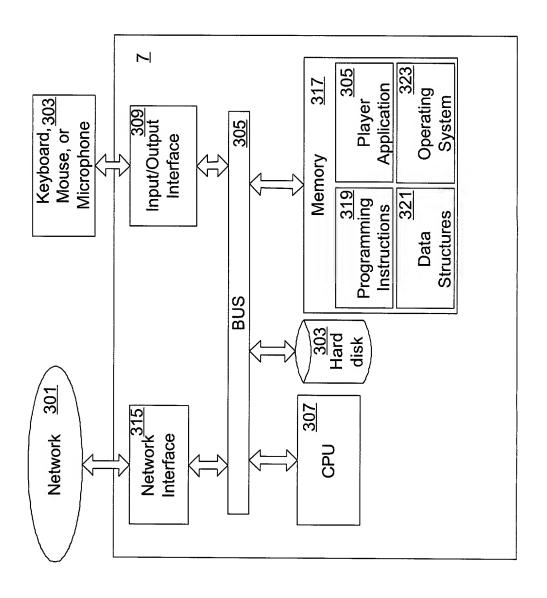


Figure 20

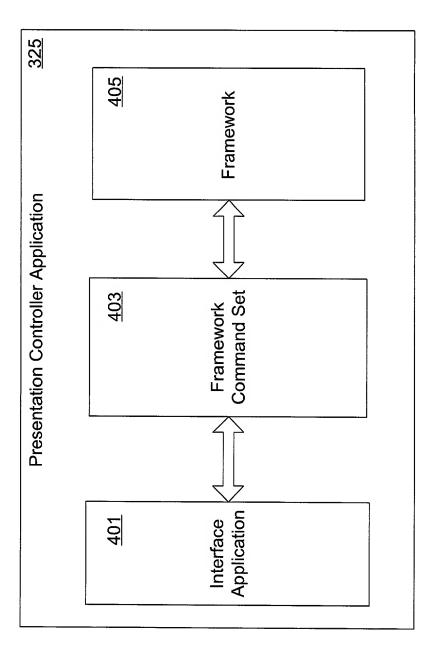


Figure 21

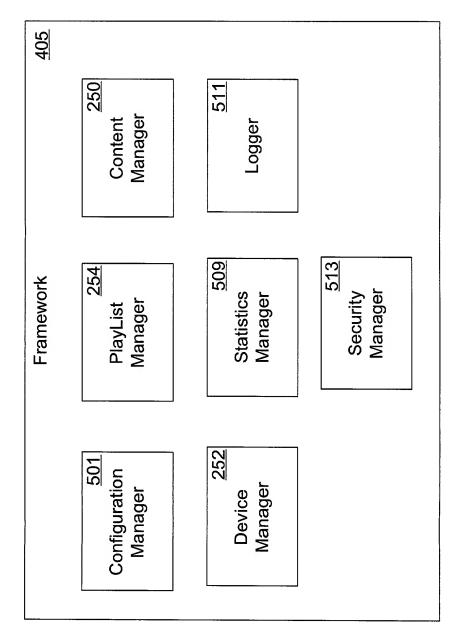
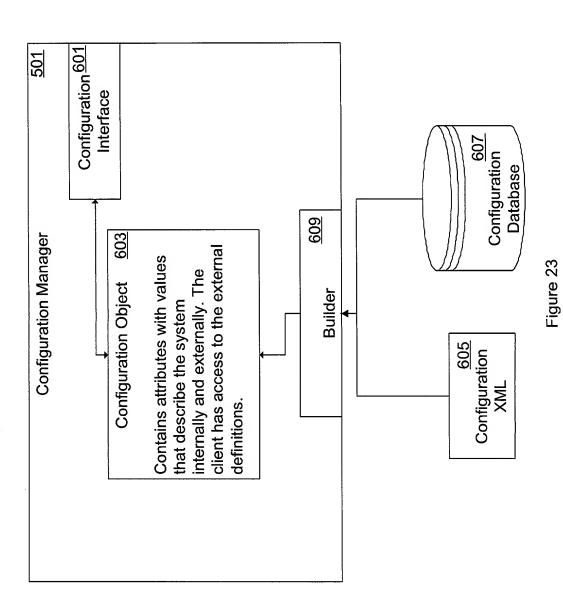
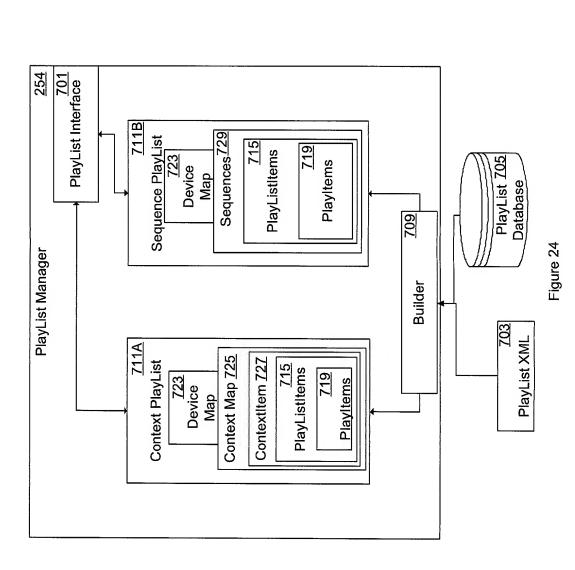


Figure 22





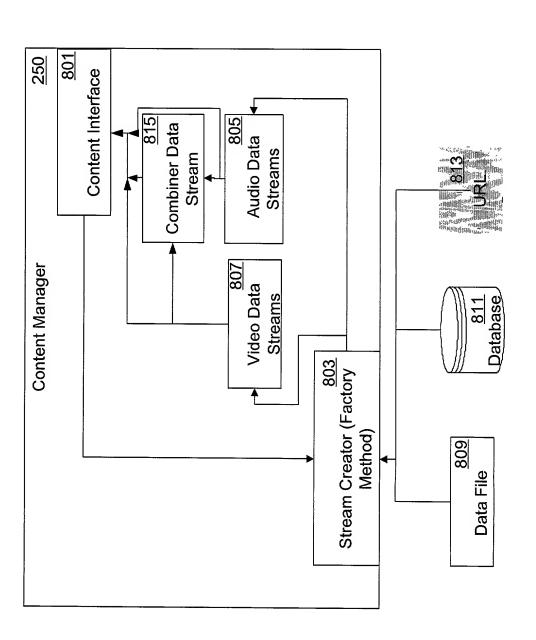
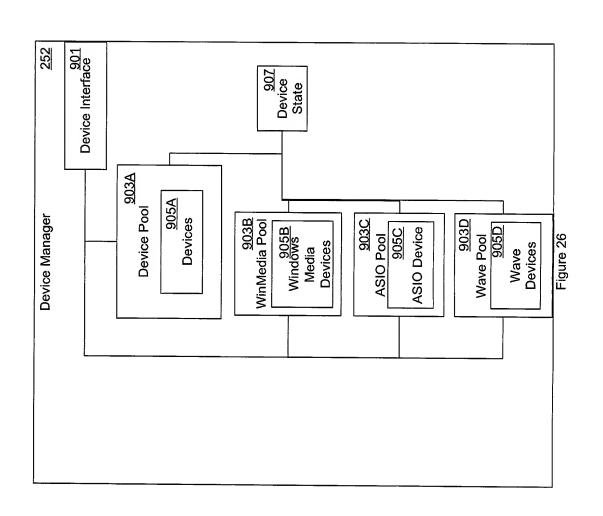


Figure 25



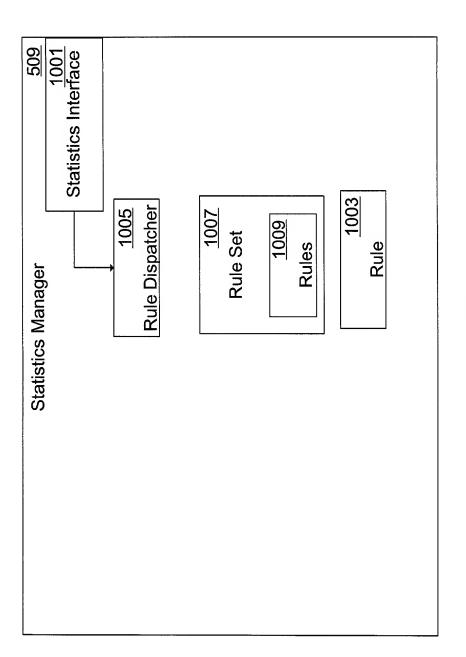


Figure 27

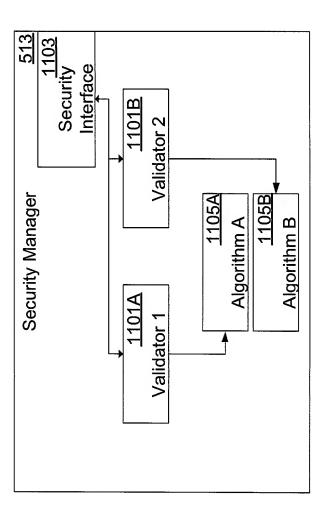


Figure 28

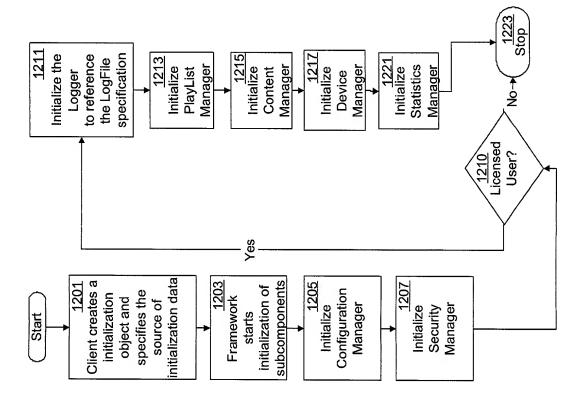
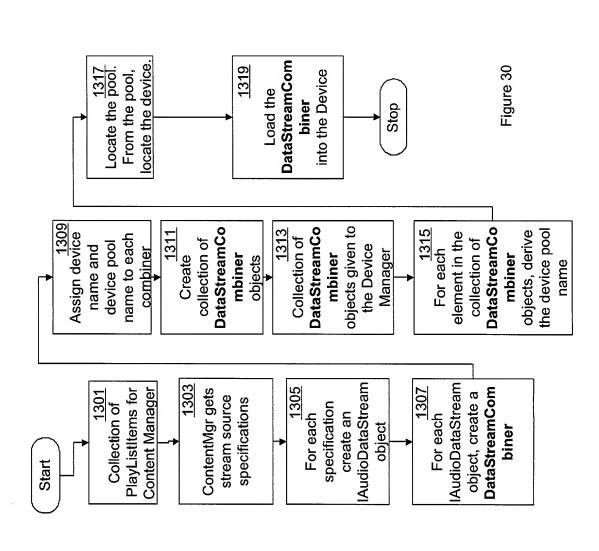


Figure 29



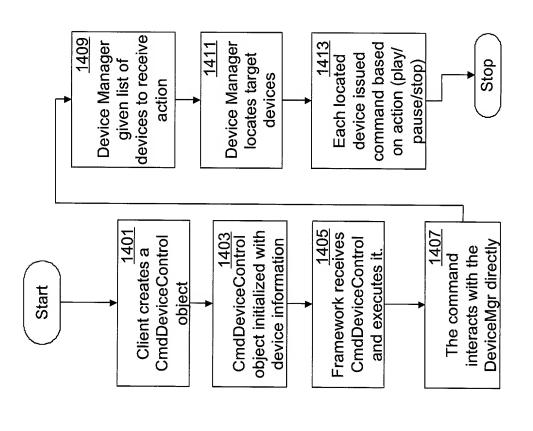


Figure 31

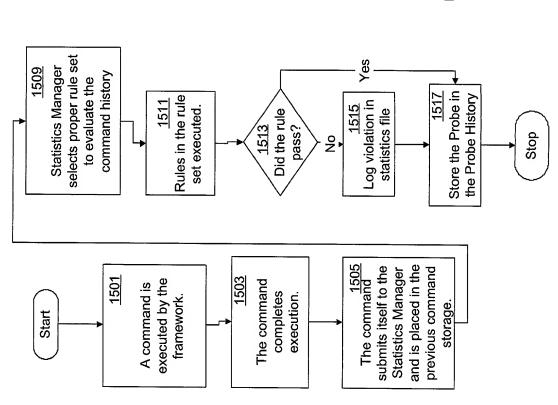


Figure 32

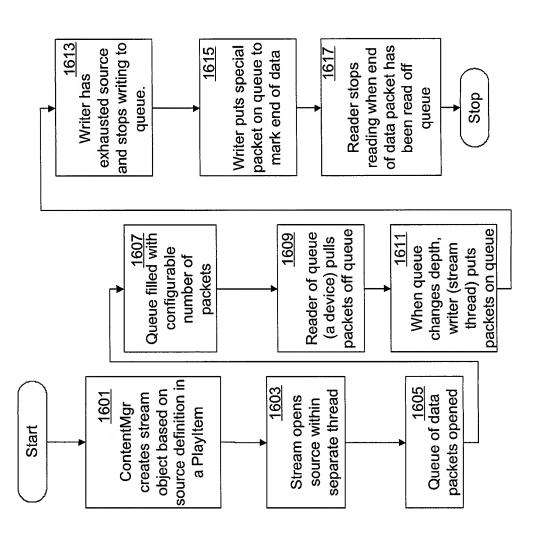


Figure 33

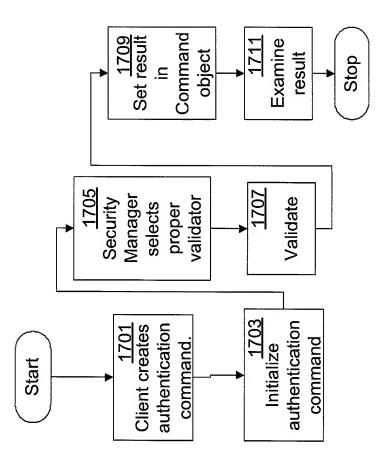


Figure 34

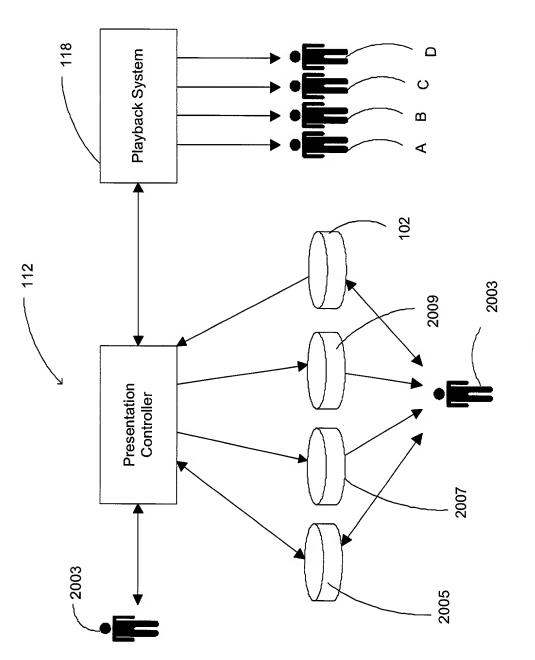


Figure 35

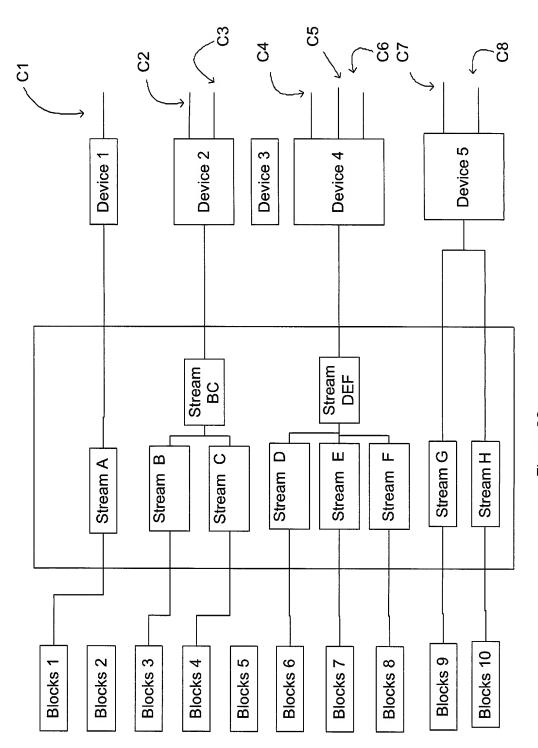


Figure 36

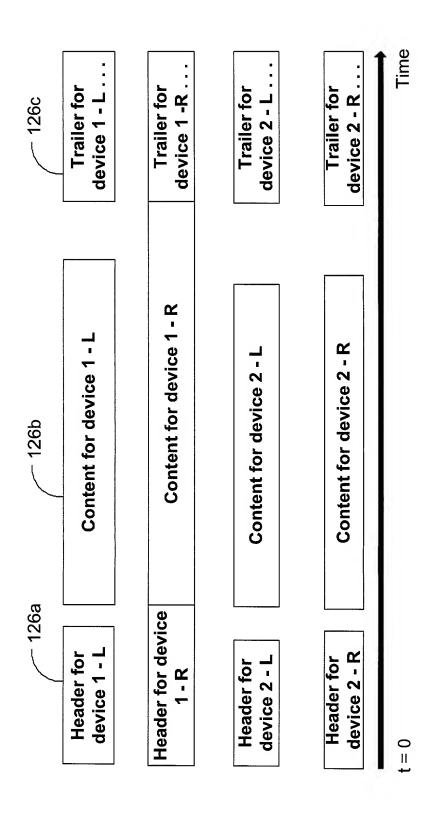


Figure 37

126c	Trailer for device 1 - L	Trailer for device 1 -R	Trailer for device 2 - L	Trailer for device 2 - R	Time
126b	Content for device 1 - L	Content for device 1 - R	Content for device 2 - L Tra	Content for device 2 - R	
126a	Header for device 1 - L	Header for device 1 - R	Header for device 2 - L	Header for device 2 - R	t = 0

Figure 38

